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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/673,310	09/29/2003	Tie Jiang Wu	10112951	1517		
34283 75	590 06/09/2005		EXAMINER			
•	LAW OFFICE VAY, 3RD FLOOR	EVERHART, CARIDAD				
	CA, CA 90404		ART UNIT	PAPER NUMBER		
			2891			
			DATE MAILED: 06/09/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application	No.	Applicant(s)			
Office Action Summary		10/673,310		WU ET AL.			
		Examiner		Art Unit			
,		Caridad M. E	verhart	2891			
The MAILING DATE of Period for Reply	of this communication app	pears on the co	over sheet with the c	orrespondence ad	dress		
A SHORTENED STATUTO THE MAILING DATE OF TH  - Extensions of time may be available after SIX (6) MONTHS from the mail  - if the period for reply specified above - if NO period for reply is specified above - Failure to reply within the set or extered any reply received by the Office laterearned patent term adjustment. See	HIS COMMUNICATION. under the provisions of 37 CFR 1.1 ing date of this communication. is less than thirty (30) days, a repl vve, the maximum statutory period v inded period for reply will, by statute than three months after the mailing	136(a). In no event, ly within the statutory will apply and will ex e, cause the applicat	nowever, may a reply be tim minimum of thirty (30) day: pire SIX (6) MONTHS from on to become ABANDONE	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).	<i>r.</i> ommunication.		
Status							
1) Responsive to comm	unication(s) filed on 30 M	March 2005					
2a) ☐ This action is <b>FINAL</b> .	·						
/ <del></del>	<del>-</del>						
,	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims					•		
4)⊠ Claim(s) <u>1-8</u> is/are pe 4a) Of the above clain 5)□ Claim(s) is/are 6)⊠ Claim(s) <u>1-5</u> is/are rej 7)□ Claim(s) is/are 8)□ Claim(s) are si	n(s) <u>6-8</u> is/are withdrawn allowed. ected. objected to.						
Application Papers							
	n is/are: a) acc est that any objection to the heet(s) including the correc	cepted or b)  drawing(s) be the drawing displayed the displayed displayed the displayed displaye	neld in abeyance. See if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CF			
Priority under 35 U.S.C. § 119							
12) △ Acknowledgment is m  a) △ All b) △ Some * c  1. △ Certified copies  2. △ Certified copies  3. △ Copies of the c	ade of a claim for foreign  None of:  of the priority document of the priority document ertified copies of the prior the International Burea	ts have been r ts have been r prity document nu (PCT Rule 1	eceived. eceived in Applicati s have been receive 7.2(a)).	on No ed in this National	Stage		
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1) Notice of References Cited (PTC 2) Notice of Draftsperson's Patent I		4)	Interview Summary Paper No(s)/Mail D				
Notice of Draitsperson's Patent (     Information Disclosure Statemen Paper No(s)/Mail Date		,	Notice of Informal F		)-152)		

## Election/Restrictions

Applicant's election without traverse of Group I in the reply filed on 3-30-2005 is acknowledged.

Claims 6-8 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected Group II, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 3-30-2005.

## Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 3 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 3 does not further limit the claim from which it depends because claim 3 does not recite structure limitations.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1,2, and 5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wang(US 2004/0153275A1) in view of Lee(US 5,977,558).

Wang discloses first and second cell structures as shown in Fig. 9, in which the first and second DRAM cell structures in a memory device are labeled 71 and 73(paragraph 0085) and there are pads 1 through 4. There is an active area shown above these structures. The test device is for the purpose of detecting the alignment of the active devices in that the openings must be aligned properly in order to prevent

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bridging, which is detected by the test device(paragraph 0044). In addition, the disclosure made by Wang encompasses vertical transistors because it is disclosed that the transistors include capacitors that are stacked and that are in trenches which are etched in the layers(paragraph 0056). The current can be determines(paragraph 0087 and 0092).

Wang is silent with respect to the test structures being in the scribe region and with respect to resistance being determined.

Lee discloses the placing of test structures in the saw kerf regions, which are the scribe regions(col. 2, lines 16-21).

It would have been obvious to one of ordinary skill in the art at the time of the invention to have formed the test structures taught by Wang in the kerf regions as taught by Lee in order to save space on the wafer.

It would have been obvious to one of ordinary skill in the art at the time of the invention to have determined resistance because it is well known in the art that the current and resistance are related.

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Wang in view of Lee as applied to claim 1 above, and further in view of Jarvis(US 6,429,452B1).

Wang is silent with respect to the pads being polysilicon.

Jarvis discloses a test structure in which the pads are polysilicon(col. 4,lines 59-67 and col. 5,lines 1-8).

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It would have been obvious to one of ordinary skill in the art at the time of the invention to have formed the pads of polysilicon in order to simplify the production of the pads as taught by Jarvis because the polysilicon layer used to form the bit lines could then be used to also form the pads.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Caridad M. Everhart whose telephone number is 571-272-1892. The examiner can normally be reached on Monday through Fridays 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, B. Baumeister can be reached on 571-272-1722. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

C. Everhart 6-7-2005

CARIDAD EVERHART PRIMARY EXAMINED

C. hurhart